

A B S T R A C T

A VOLTAGE/VOLTAGE CONVERTER FOR INTEGRATED CIRCUITS

5 A voltage/voltage converter for integrated circuits
is characterized in that it presents a multistage
symmetrical structure and comprises at least one input
stage constituted by a clock booster circuit (CB) of
symmetrical structure which delivers two output voltages,
10 a voltage multiplier stage of symmetrical structure
comprising two voltage multiplier circuits (CM_i ; CM_{ip})
respectively connected in two branches (B_1 ; B_2) of the
converter and having applied respectively thereto the
output voltages from the first stage, and an output stage
15 (S) constituted by a multiplexer circuit (MX) having
applied thereto the two output voltages from the voltage
multiplier stage. The invention is particularly applied
to EEPROMs and to low-voltage integrated circuits.

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35 Translation of the title and the abstract as they were when originally filed by the
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